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EXAMINER

CHOWDHURY, NIGAR

ART UNIT

PAPER NUMBER

2484

NOTIFICATION DATE

DELIVERY MODE

05/12/2011

ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

chicago.patents@klgates.com

DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed on 04/18/2011 have been fully considered but they are not persuasive.
2. In re pages 9-10, applicant argues that Miller discloses matrix switch which allows multiple inputs to be directed to multiple outputs. In the multi-media matrix switch the primary output of the matrix switch is a data stream that defines an editing project created by a user. When the user creates the editing project, the user can select from a number of different multimedia clips which can be assembled into a unique presentation. Miller fails to disclose a first alternate track including video data equivalent to video data obtained by performing a process according to the effect track, the first alternate track being used in response to the effect track being unprocessable, as recited in claim 1.

In response, the examiner respectfully disagrees. Miller et al. discloses from col. 6 lines 34-37 that "RAM 210 typically contains data and/or program modules that are immediately accessible to and/or presently be operated on by processing unit(s) 202"; col. 13 lines 63-col. 14 lines 57 that "...when a user creates an editing project, they can select from a number of different multimedia clips that they can then assemble into a unique presentation.....video source can have transitions.....and effects.....transition object is a way to change between two or more sources.....effect object can operate on a single source or on a composite of sources.....effect can comprises a black-and-white effect....."; fig. 21, col. 19 lines 35-51 that ".....the data structure includes three tracks,

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each of which contains one of the sources. The sources are arranged in the tree structure in the order of their priority, starting with the lowest priority source on the left and proceeding to the right...an effect...associated with source B....transition attached to.....source C” Miller et al. discloses in fig. 2, a memory device (RAM) storing instructions which when executed by the processor. Miller et al. also discloses a user-defined editing project, wherein, when a user creates an editing project, they can select from a number of different multimedia clips that they can then assemble into a unique presentation. Each individual clip represents a source of digital data or a source stream. Video source can have transitions and effects applied on them. The sources are arranged in the tree structure in the order of their priority. Furthermore, Miller et al. discloses a first alternate track, for example, in fig. 21, a alternate track is “B” which includes video data and being used in response to the track “A” which being unprocessable. Source “A” doesn’t have any transition object to change between two or more sources or any effect object video stream presentation format. We can broadly interpret “unprocessable” as which is not process by adding any transition object or any effect object. The priority level of the source A, B, and C, starting with lowest priority source from A to C. Source A has low priority than source B and C. Therefore, the first alternate track is meet by the source B or C including video data equivalent to video data obtained by performing a priority process, wherein, the first alternate track B or C being used in response to the effect track A being uprocessable.

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3. Claims 7, 9-11, 13 are rejected for the same reason as discussed in the corresponding paragraph 2 above.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to NIGAR CHOWDHURY whose telephone number is (571)272-8890. The examiner can normally be reached on 9-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thai Tran can be reached on 571-272-7382. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

NC
05/05/2011

/Thai Tran/
Supervisory Patent Examiner, Art Unit 2484